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LMV712 LOW-POWER LOW-NOISE HIGH-OUTPUT RRIO DUAL OPERATIONAL AMPLIFIER WITH INDEPENDENT SHUTDOWN

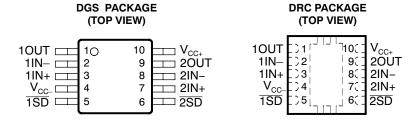
SLOS485-JANUARY 2006

FEATURES

- 5-MHz Gain Bandwidth Product
- 5-V/μs Slew Rate
- Low Noise: 20 nV/√Hz
- 1.22-mA/Channel Supply Current
- V_{OS} < 3 mV Max
- Low Supply Voltage: 2.7 V to 5 V
- Rail-to-Rail Inputs and Outputs
- Unity Gain Stable
- 1.5-μA Shutdown I_{CC}
- 2.2-μs Turn On

APPLICATIONS

- Power-Amplifier Control Loops
- Cellular Phones
- Portable Equipment
- Wireless LANs
- Radio Systems
- Cordless Phones



DESCRIPTION/ORDERING INFORMATION

The LMV712 dual operational amplifier is a high-performance BiCMOS operational amplifier intended for applications requiring rail-to-rail inputs, combined with speed and low noise. The device offers a bandwidth of 5 MHz, a slew rate of 5 V/μ s, and operates with capacitive loads of up to 200 pF without oscillation.

The LMV712 offers two independent shutdown ($\overline{1SD}$, $\overline{2SD}$) pins. This feature allows disabling of each device separately and reduces the supply current to less than 1 μ A typical. The output voltage rapidly and smoothly ramps up with no glitch as the amplifier comes out of the shutdown mode.

The LMV712 is offered in the space-saving SON (DRC) package and in an MSOP (DGS) package. These packages are designed to meet the demands of small size, low power, and low cost required by cellular phones and similar battery-operated portable electronics.

ORDERING INFORMATION

T _A	PACK	AGE ⁽¹⁾	ORDERABLE PART NUMBER	TOP-SIDE MARKING
	MSOP – DGS	Reel of 2500	LMV712IDGSR	RNB
_40°C to 85°C	MSOP - DGS	Reel of 250	LMV712IDGST	HIND
-40 C to 85 C	SON – DRC		LMV712IDRCR	PREVIEW
	SUN - DUC	Reel of 250	LMV712IDRCT	FNEVIEW

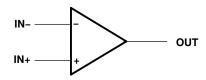
(1) Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.



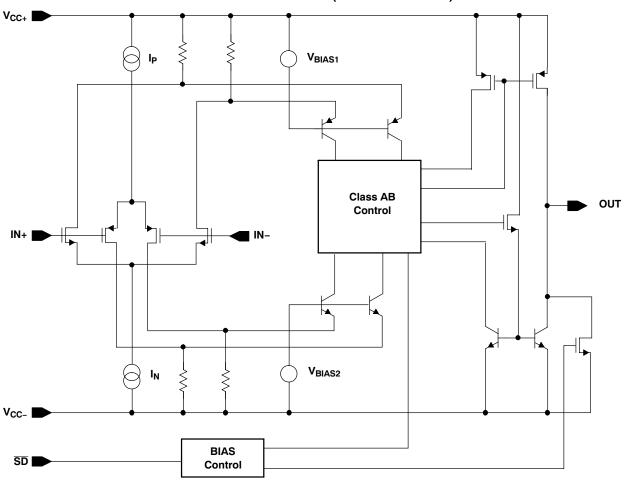
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SYMBOL (EACH AMPLIFIER)



SIMPLIFIED SCHEMATIC (EACH AMPLIFIER)





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Absolute Maximum Ratings(1)

over operating free-air temperature range (unless otherwise noted)

			MIN	MAX	UNIT
$V_{CC+} - V_{CC-}$	Supply voltage (2)			5.5	V
V _{ID}	Differential input voltage (3)		±Supply voltage	V	
VI	Input voltage range (any input)		V _{CC} - 0.4	$V_{CC+} + 0.4$	V
Vo	Output voltage range	V _{CC} - 0.4	V _{CC+} + 0.4	V	
I	Input current ⁽⁴⁾		±10	mA	
Io	Output current			±50	mA
0	Dealers thermal impedance (5) (6)	DGS package		165	°C/W
θ_{JA}	Package thermal impedance (5) (6)	DRC package		TBD	C/VV
T _J	Operating virtual junction temperature			150	°C
T _{stg}	Storage temperature range		-65	150	°C

- (1) Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
- (2) All voltage values (except differential voltages and V_{CC} specified for the measurement of I_{OS}) are with respect to the network GND.
- (3) Differential voltages are at IN+ with respect to IN-.
- (4) Excessive input current will flow if a differential input voltage in excess of approximately 0.6 V is applied between the inputs, unless some limiting resistance is used.
- (5) Maximum power dissipation is a function of $T_J(max)$, θ_{JA} , and T_A . The maximum allowable power dissipation at any allowable ambient temperature is $P_D = (T_J(max) T_A)/\theta_{JA}$. Operating at the absolute maximum T_J of 150°C can affect reliability.
- (6) The package thermal impedance is calculated in accordance with JESD 51-7.

ESD Protection

	TYP	UNIT
Human-Body Model	1500	V
Machine Model	150	V

Recommended Operating Conditions

		MIN	MAX	UNIT
$V_{CC+} - V_{CC-}$	Supply voltage	2.7	5	V
T _A	Operating free-air temperature	-40	85	°C

LMV712

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Electrical Characteristics

 $\rm V_{CC_{+}}$ = 2.7 V, $\rm V_{CC_{-}}$ = GND, $\rm V_{CM}$ = 1.35 V, and $\rm R_{L}$ > 1 $\rm M\Omega$ (unless otherwise noted)

	PARAMETER	TEST COND	DITIONS	T _A	MIN	TYP	MAX	UNIT	
V _{IO}	Input offset voltage	V _{CM} = 0.85 V and 1.85	V	25°C		0.4	3	mV	
V 10	Input onset voltage	VCM = 0.03 V and 1.03		-40°C to 85°C			3.2	1111	
I _{IB}	Input bias current			25°C		5.5	115	pА	
'IB	input bias current			–40°C to 85°C			130	PA	
CMRR	Common-mode	0 ≤ V _{CM} ≤ 2.7 V		25°C	50	75		dB	
OWNTH	rejection ratio	0 2 VCM 2 2.1 V		–40°C to 85°C	45			ub.	
			V _{CM} = 0.85 V	25°C	70	90			
PSRR	Power-supply	$2.7 \text{ V} \le \text{V}_{\text{CC+}} \le 5 \text{ V}$	VCM = 0.00	-40°C to 85°C	68		dB		
Ortit	rejection ratio	7.7 V 3 VCC+ 3 3 V	V _{CM} = 1.85 V	25°C	70	90		u u u	
			v _{CM} = 1.85 v	–40°C to 85°C	68				
CMVR	Common-mode	CMRR ≥ 50 dB		25°C		-0.3	-0.2	V	
	voltage range			25 0	2.9	3		v	
		Causaina V 0		25°C	15	25			
	Output	Sourcing V _O = 0		-40°C to 85°C	12				
sc	short-circuit current(1)	2		25°C	25	50		mA .	
		Sinking V _O = 2.7 V		-40°C to 85°C	22				
			1	25°C	2.62	2.68			
			V _{OH}	-40°C to 85°C	2.6				
		$R_L = 10 \text{ k}\Omega \text{ to } 1.35 \text{ V}$		25°C		0.01	0.12		
			V _{OL}	-40°C to 85°C			0.15	V	
√ _O	Output voltage swing			25°C	2.52	2.55			
			V _{OH}	-40°C to 85°C	2.5				
		$R_L = 600 \Omega \text{ to } 1.35 \text{ V}$		25°C		0.05	0.23		
			V _{OL}	-40°C to 85°C			0.3		
V _{O(SD)}	Output voltage level in shutdown mode			25°C		10	200	mV	
		ON mode		25°C		1.22	1.7	mA	
ı	Supply current	ON mode		–40°C to 85°C			1.9	IIIA	
I _{CC}	per channel	Chutdown modo		25°C		0.12	1.5		
		Shutdown mode	-40°C to 85°C			2	μΑ		
		Sourcing $R_L = 10 \text{ k}\Omega$,		25°C	80	115			
		$V_0 = 1.35 \text{V} \text{ to } 2.3 \text{V}$		-40°C to 85°C	76				
		Sinking $R_L = 10 \text{ k}\Omega$,		25°C	80	113			
	Large-signal	$V_0 = 0.4 \text{ V} \text{ to } 1.35 \text{ V}$		-40°C to 85°C	76				
A _{VOL}	voltage gain	Sourcing $R_L = 600 \Omega$,		25°C	80	97		dB	
		V _O = 1.35 V to 2.2 V		-40°C to 85°C	76				
		Sinking $R_L = 600 \Omega$,		25°C	80	100			
		$V_0 = 0.5 \text{ V to } 1.35 \text{ V}$		-40°C to 85°C	76				
		ON mode			2.4 to 2.7	2 to 2.7			
V_{SD}	Shutdown pin voltage	Shutdown mode		25°C	0 to 0.8	0 to 1		V	
GBWP	Gain bandwidth product			25°C		5		MHz	
SR ⁽²⁾	Slew rate			25°C		5		V/μs	
Φ_{m}	Phase margin			25°C		60		0	
V _n	Input referred voltage noise	f = 1 kHz	25°C		20		nV/√H:		

Shorting the output to either supply rail adversely affects reliability. Number specified is the slower of the positive and negative slew rates. (2)





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Electrical Characteristics (continued)

 $\rm V_{CC_{+}}$ = 2.7 V, $\rm V_{CC_{-}}$ = GND, $\rm V_{CM}$ = 1.35 V, and $\rm R_{L}$ > 1 $\rm M\Omega$ (unless otherwise noted)

	PARAMETER	TEST CONDITIONS	T _A	MIN	TYP	MAX	UNIT
_	Turn-on time from		25°C		2.2	4	_
I (on)	shutdown		25 C			4.6	μS

LMV712

LOW-POWER LOW-NOISE HIGH-OUTPUT RRIO DUAL OPERATIONAL AMPLIFIER WITH INDEPENDENT SHUTDOWN



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Electrical Characteristics

 $\rm V_{CC_{+}}$ = 5 V, $\rm V_{CC_{-}}$ = GND, $\rm V_{CM}$ = 2.5 V, and $\rm R_{L}$ > 1 $\rm M\Omega$ (unless otherwise noted)

	PARAMETER	TEST CON	DITIONS	T _A	MIN	TYP	MAX	UNIT	
	Input offset voltage	V _{CM} = 0.85 V and 1.8		25°C		0.4	3	mV	
V _{IO}	input offset voltage	V _{CM} = 0.65 V and 1.6	oo v	–40°C to 85°C			3.2	IIIV	
ı	Input biog gurrent			25°C		5.5	115	n ^	
IB	Input bias current			–40°C to 85°C			130	pA	
CMRR	Common-mode	0.4// 45//		25°C	50	80		dB	
	rejection ratio	$0 \le V_{CM} \le 5 \text{ V}$		–40°C to 85°C	45			uБ	
			V _{CM} = 0.85 V	25°C	70	90			
PSRR	Power-supply	271/21/251/	V _{CM} = 0.85 V	–40°C to 85°C	68			dB	
SHH	rejection ratio	$2.7 \text{ V} \leq \text{V}_{\text{CC+}} \leq 5 \text{ V}$	\/ 1.0E.\/	25°C	70	90		uБ	
			V _{CM} = 1.85 V	-40°C to 85°C	68				
CMVR	Common-mode	CMRR ≥ 50 dB	25°C		-0.3	-0.2	V		
	voltage range			25 0	5.2	5.3		V	
		Sourcing V 0		25°C	20	35			
	Output	Sourcing V _O = 0		–40°C to 85°C	18			mΛ	
SC	short-circuit current (1)	Sinking V E V		25°C	25	50		mA	
		Sinking V _O = 5 V		–40°C to 85°C	21				
			V	25°C	4.92	4.98			
		B 10 k0 to 0 5 V	V _{OH}	–40°C to 85°C	4.9				
		$R_L = 10 \text{ k}\Omega \text{ to } 2.5 \text{ V}$	V	25°C		0.01	0.12	V	
,	Output voltage ewing		V _{OL}	-40°C to 85°C			0.15		
V _O	Output voltage swing		V	25°C	4.82	4.85			
		D 600 O to 0.5 V	V _{OH}	-40°C to 85°C	4.8				
		$R_L = 600 \Omega \text{ to } 2.5 \text{ V}$	V	25°C		0.05	0.23		
			V _{OL}	-40°C to 85°C			0.3		
V _{O(SD)}	Output voltage level in shutdown mode			25°C		10	200	mV	
		ON made		25°C		1.17	1.7	mΔ	
	Supply current	ON mode		-40°C to 85°C			1.9	mA	
CC	per channel	Chutdown made		25°C		0.12	1.5	^	
		Shutdown mode		-40°C to 85°C			2	μΑ	
		Sourcing $R_L = 10 \text{ k}\Omega$,		25°C	80	130			
		$V_0 = 2.5 \text{ V to } 4.6 \text{ V}$		-40°C to 85°C	76				
		Sinking $R_L = 10 \text{ k}\Omega$,		25°C	80	130			
	Large-signal	$V_0 = 0.4 \text{ V to } 2.5 \text{ V}$		-40°C to 85°C	76			40	
A _{VOL}	voltage gain	Sourcing R _L = 600 Ω,		25°C	80	110		dB	
		V _O = 2.5 V to 4.6 V		-40°C to 85°C	76				
		Sinking $R_L = 600 \Omega$,		25°C	80	107			
		$V_0 = 0.4 \text{ V} \text{ to } 2.5 \text{ V}$		-40°C to 85°C	76			1	
,	Ob tale a discollege	ON mode		0500	4.5 to 5	3.5 to 5		.,	
/ _{SD}	Shutdown pin voltage	Shutdown mode		25°C	0 to 0.8	0 to 1.5		V	
BWP	Gain bandwidth product			25°C		5		MHz	
SR (2)	Slew rate			25°C		5		V/μs	
Φ _m	Phase margin			25°C	,	60		0	
V _n	Input referred voltage noise	f = 1 kHz		25°C		20		nV/√H:	

Shorting the output to either supply rail adversely affects reliability. Number specified is the slower of the positive and negative slew rates. (2)





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Electrical Characteristics (continued)

 $\rm V_{CC_{+}}$ = 5 V, $\rm V_{CC_{-}}$ = GND, $\rm V_{CM}$ = 2.5 V, and $\rm R_{L}$ > 1 $\rm M\Omega$ (unless otherwise noted)

	PARAMETER	TEST CONDITIONS	T _A	MIN	TYP	MAX	UNIT
_	Turn-on time		25°C		1.6	4	
I (on)	from shutdown		25 C			4.6	μS



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TYPICAL CHARACTERISTICS

GRAPH PREVIEWS

Supply Current per Channel vs Supply Voltage (ON Mode)

Supply Current per Channel vs Supply Voltage (Shutdown Mode)

Input Offset Voltage vs Common-Mode Voltage

Bias Current vs Common-Mode Voltage Over Temperature

Output Positive Swing vs Supply Voltage ($R_1 = 600 \Omega$)

Output Negative Swing vs Supply Voltage ($R_1 = 600 \Omega$)

Sourcing Current vs Output Voltage (V_{CC} = 2.7 V)

Sourcing Current vs Output Voltage (V_{CC} = 5 V)

Sinking Current vs Output Voltage (V_{CC} = 2.7 V)

Sinking Current vs Output Voltage (V_{CC} = 5 V)

PSRR vs Frequency (V_{CC} = 2.7 V)

PSRR vs Frequency (V_{CC} = 5 V)

CMRR vs Frequency (V_{CC} = 2.7 V)

CMRR vs Frequency ($V_{CC} = 5 \text{ V}$)

Open-Loop Frequency Response vs R_L (V_{CC+} = 2.7 V)

Open-Loop Frequency Response vs R_L ($V_{CC_{\pm}} = 5 \text{ V}$)

Open-Loop Frequency Response vs C_1 ($V_{CC+} = 2.7 \text{ V}$)

Open-Loop Frequency Response vs C_L (V_{CC±} = 5 V)

Voltage Noise vs Frequency ($V_{CC} = 2.7 \text{ V}$)

Voltage Noise vs Frequency (V_{CC} = 5 V)

Non-Inverting Large Signal Pulse Response (V_{CC} = 2.7 V)

Non-Inverting Large Signal Pulse Response (V_{CC} = 5 V)

Non-Inverting Small Signal Pulse Response (V_{CC} = 2.7 V)

Non-Inverting Small Signal Pulse Response (V_{CC} = 5 V)

Inverting Large Signal Pulse Response (V_{CC} = 2.7 V)

Inverting Large Signal Pulse Response (V_{CC} = 5 V)

Inverting Small Signal Pulse Response (V_{CC} = 2.7 V)

Inverting Small Signal Pulse Response (V_{CC} = 5 V)

Turn-On Response Time ($V_{CC} = 5 \text{ V}$)

Input Common-Mode Capacitance vs Common-Mode Voltage (V_{CC} = 5 V)





com 12-Oct-2007

PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
LMV712IDGSR	ACTIVE	MSOP	DGS	10	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LMV712IDGSRG4	ACTIVE	MSOP	DGS	10	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LMV712IDGST	ACTIVE	MSOP	DGS	10	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LMV712IDGSTG4	ACTIVE	MSOP	DGS	10	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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TAPE AND REEL INFORMATION





A0	Dimension designed to accommodate the component width
В0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal

	Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
L	MV712IDGSR	MSOP	DGS	10	2500	330.0	12.4	5.3	3.3	1.3	8.0	12.0	Q1





*All dimensions are nominal

Ī	Device	Device Package Type		Pins	SPQ	Length (mm)	Width (mm)	Height (mm)	
	LMV712IDGSR	MSOP	DGS	10	2500	370.0	355.0	55.0	

DGS (S-PDSO-G10)

PLASTIC SMALL-OUTLINE PACKAGE



NOTES:

- A. All linear dimensions are in millimeters.
- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion.
- D. Falls within JEDEC MO-187 variation BA.



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